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FOR A SYNCHRONOUS SYSTEM

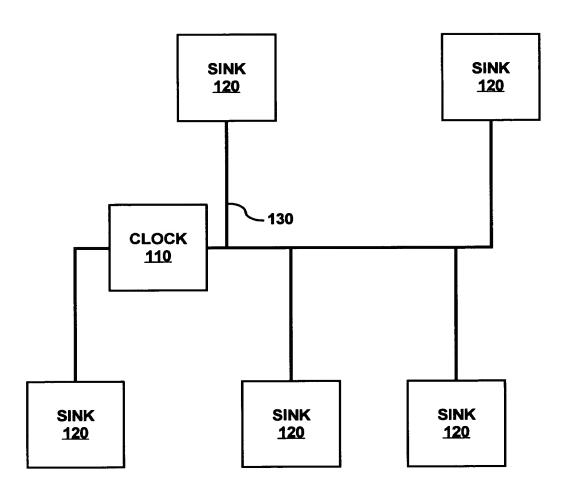
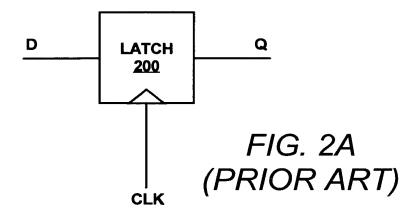


FIG. 1 (PRIOR ART)

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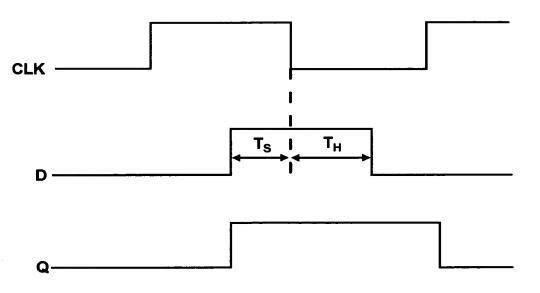


FIG. 2B (PRIOR ART)